

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L3	601294	saito-t\$.in. or toshiba.as.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/12/14 10:04
L4	1	I3 and (processor and peripheral and debug).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/12/14 10:05
L5	91	(((((((((714/30 or 714/25 or 714/733 or 714/734 or 700/26). ccls.) and chip) and (processor or controller)) and (program with count\$3)) and register) and (peripheral or function\$3)) and (select\$4 or multiplex\$3)) and (select\$4 with control\$3)) and @ad<"20010710"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/12/14 10:05
L6	9	(((((lsi or(logic adj circuit) or (integrated adj circuit) or MPU or microprocessror) and ((peripheral or auxilliary) adj circuit)) and (one adj chip)) and debug) and @ad<"20000710"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/12/14 10:07
L7	91	(((((((((714/30 or 714/25 or 714/733 or 714/734 or 700/26). ccls.) and chip) and (processor or controller)) and (program with count\$3)) and register) and (peripheral or function\$3)) and (select\$4 or multiplex\$3)) and (select\$4 with control\$3)) and @ad<"20010710"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/12/14 10:07